

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

OKI.631

Application Number

NEW

Applicant(s)

Noriyuki MIURA

Filing Date

December 9, 2003

Group Art Unit

To Be Assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
J. G.	A	6,093,592	07/25/2000	NAKABAYASHI et al.	438	221	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
J. G.	B	63-313865	12/21/1988	JAPAN			Abstract	
J. G.	C	07-211902	08/11/1995	JAPAN			Abstract	
J. G.	D	08-051198	02/20/1996	JAPAN			Abstract	
J. G.	E	2001-135821	05/18/2001	JAPAN			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

J. G.	F	Lisa T. Su et al., "OPTIMIZATION OF SERIES RESISTANCE IN SUB-0.2 μ m SOI MOSFETs", 1993 IEEE, pgs. 723-726.					
J. G.	G	Neal Kistler et al., "SCALING BEHAVIOR OF SUB-MICRON MOSFETs ON FULLY DEPLETED SOI", Solid-State Electronics Vol. 39, No. 4, 1996, pgs. 445-454.					

EXAMINER		DATE CONSIDERED	12/9/04
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.